

REMARKS

A. GENERALLY

Claims 2-4 and 7-14 remain in the Application. Claims 2-4, 8 and 10-14 have been amended. Claims 1, 5, and 15-28 were previously canceled. Claim 6 is canceled herewith. No new matter has been added.

B. CLAIMS REJECTIONS PURSUANT to 35 U.S.C. § 103

Claims 2, 4, 7, and 9-13 have been rejected under 35 U.S.C. § 103 as being unpatentable over U.S. Patent 6,725,374 issued to Patarin et al. (hereinafter, "Patarin") in view of U.S. Patent 6,725,374 issued to Jahnich et al. (hereinafter, "Jahnich"). Claims 8 and 14 have been rejected under 35 U.S.C. § 103 as being unpatentable over Patarin in view of Jahnich and further in view of U.S. Patent 6,490,353 issued to Tan (hereinafter, "Tan").

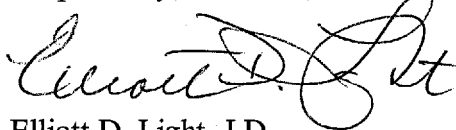
Claims 3 and 6 were objected to but would be allowable if rewritten in independent form.

Applicant has amended independent claim 2 and independent claim 10 to recite the additional limitation, "wherein the selection as to which of the one or more processors performs a useful operation is randomly controlled." This limitation reflects the allowable subject matter identified in claim 3. The claims have also been amended to recite "two or more processors" as a generic term that encompasses CPUs and co-processors. Claims 3 and 14 have been amended to recite that the two or more processors comprise a CPU and one or more co-processors.

C. CONCLUSION

Applicant respectfully submits that the claims as currently listed are in condition for allowance. Applicant requests that this response be entered and that the current rejections of the claims and objections to the disclosure now pending in this application be withdrawn in view of the above amendments, remarks and arguments.

Respectfully submitted,



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